

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	222	((microinstruction micro-instruction) with buffer) and valid	USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/22 16:22
L2	22	(queue buffer) with (multithread\$4 multi-thread\$4) with (instruction section portion part)	USPAT; EPO; JPO	OR	ON	2004/11/22 16:22
L3	53	((microinstruction micro-instruction) with queue) and valid	USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/22 16:22
L4	94	(microinstruction micro-instruction) with queue	USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/22 16:22
L5	52	microinstruction near4 cache	USPAT	OR	ON	2004/11/22 16:22
L6	27	((((microinstruction micro-instruction) with buffer) same cache) and valid	USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/22 16:22
L7	8	(queue buffer) with multithreaded with instruction	USPAT; EPO; JPO	OR	ON	2004/11/22 16:22
L8	11	(microinstruction near4 cache) and (macroinstruction near4 cache)	USPAT	OR	ON	2004/11/22 16:23
L9	52	macroinstruction near4 cache	USPAT	OR	ON	2004/11/22 16:23
L10	125	microinstruction with cache	USPAT	OR	ON	2004/11/22 16:23
S1	2	(microinstruction micro-instruction) with queue with valid	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/01/16 14:38
S2	2	("4682284" "5954815").pn.	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/01/21 14:00
S3	0	"08445563".ap.	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/01/21 14:01
S4	0	"08445563"	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/01/21 14:01
S5	4	"445563".ap.	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/01/21 14:50
S6	1701	bypass with (queue buffer)	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/01/21 14:50
S7	0	bypass with (queue buffer) with (microinstruction micro-instruction)	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/01/21 15:02

S8	5	bypass with (queue buffer) same (microinstruction micro-instruction)	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/01/21 15:02
S9	22	(queue buffer) with multithreaded	USPAT; EPO; JPO	OR	ON	2003/01/22 16:00
S10	118	microinstruction with cache	USPAT	OR	ON	2003/01/22 15:34
S11	41	macroinstruction near4 cache	USPAT	OR	ON	2003/01/22 15:52
S12	5	(microinstruction near4 cache) and (macroinstruction near4 cache)	USPAT	OR	ON	2003/01/22 15:52
S13	0	(queue buffer) with multithreaded with (portion section)	USPAT; EPO; JPO	OR	ON	2003/01/22 16:01
S14	0	(queue buffer) with multithreaded with (portion section part)	USPAT; EPO; JPO	OR	ON	2003/01/22 16:02
S15	2	(queue buffer) with multithreaded with (portion section partition\$4)	USPAT; EPO; JPO	OR	ON	2003/01/22 16:04
S16	3	(queue buffer) with multithreaded with instruction	USPAT; EPO; JPO	OR	ON	2003/01/23 10:28
S17	27	((microinstruction micro-instruction) with buffer) same cache) and valid	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/07/07 14:26
S18	47	microinstruction near4 cache	USPAT	OR	ON	2003/07/07 14:26
S19	82	(microinstruction micro-instruction) with queue	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/07/07 14:26
S20	46	((microinstruction micro-instruction) with queue) and valid	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/07/07 14:26
S21	14	(queue buffer) with (multithread\$4 multi-thread\$4) with (instruction section portion part)	USPAT; EPO; JPO	OR	ON	2003/07/07 14:27
S22	215	((microinstruction micro-instruction) with buffer) and valid	USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/07/07 14:27
S23	0	712/205.ccls	USPAT; EPO; JPO	OR	ON	2004/04/01 18:55
S24	210	712/205.ccls.	USPAT; EPO; JPO	OR	ON	2004/04/01 18:55
S25	158	712/206.ccls.	USPAT; EPO; JPO	OR	ON	2004/04/01 18:55
S26	327	712/207.ccls.	USPAT; EPO; JPO	OR	ON	2004/04/01 18:55
S27	465	712/245.ccls.	USPAT	OR	OFF	2004/11/19 18:04
S28	222	712/205.ccls.	USPAT; EPO; JPO	OR	ON	2004/11/19 18:04
S29	352	712/207.ccls.	USPAT; EPO; JPO	OR	ON	2004/11/19 18:04

S30	166	712/206.ccls.	USPAT; EPO; JPO	OR	ON	2004/11/19 18:04
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